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APPLICANTS

Atsushi Sano, Toyama, JAPAN;
 Sadayoshi Horii, Toyama, JAPAN;
 Hideharu Itanai, Toyama, JAPAN;
 Masayuki Asai, Toyama, JAPAN;

[S/N]

** CONTINUING DATA *****

This application is a 371 of PCT/JP05/00751 01/21/2005 [S/N]

** FOREIGN APPLICATIONS *****

JAPAN 2004-012885 01/21/2004 [S/N]

** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	[S/N]			
Verified and /SEAHVOSH J NIKMANESH/ Examiner's Signature		Met after Allowance [S/N] Initials	JAPAN	16	21
Acknowledged					4

ADDRESS

OLIFF & BERRIDGE, PLC
 P.O. BOX 320850
 ALEXANDRIA, VA 22320-4850
 UNITED STATES

TITLE

Manufacturing method of semiconductor device and substrate processing apparatus

FILING FEE RECEIVED 1150	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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